

Electronic Acknowledgement Receipt

EFS ID:	7417679
Application Number:	10789637
International Application Number:	
Confirmation Number:	7655
Title of Invention:	Flip chip semiconductor die internal signal access system and method
First Named Inventor/Applicant Name:	Brian S. Schieck
Customer Number:	45594
Filer:	John F. Ryan
Filer Authorized By:	
Attorney Docket Number:	NVID-P001125
Receipt Date:	14-APR-2010
Filing Date:	27-FEB-2004
Time Stamp:	23:50:34
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		NVID-P001125-SUPAMDT-4-14-10.pdf	412108 5beee888526b2dd53d41d56c7fa79c0bc7e ce4fe	yes	17

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Supplemental Response or Supplemental Amendment	1	1
	Claims	2	10
	Applicant Arguments/Remarks Made in an Amendment	11	17

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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